

The Bombay Salesian Society's
Don Bosco Institute of Technology

An Autonomous Institute Affiliated to University of Mumbai
Premier Automobiles Road, Kurla West, Mumbai – 400070

Department of Electronics and Telecommunication Engineering
Presents

The Bombay Salesian Society's
DON BOSCO INSTITUTE OF TECHNOLOGY
(An Autonomous Institute Affiliated to University of Mumbai)

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

SANKALPANA 2026

Mini Project Exhibition and Competition (SE and TE)

INNOVATE
Showcase creative ideas and smart solutions

BUILD
Turn concepts into real-world prototypes

CONNECT
Collaborate, share knowledge and grow together

IMPACT
Create technology that makes a difference

DATE
14TH MAY 2026

TIME
10:00 AM TO 1:00 PM

VENUE
C WING,
1ST FLOOR
EXTC LABORATORIES

IDEAS TODAY. IMPACT TOMORROW.
Technology is one of the greatest engines that drives change and creates new opportunities for a better tomorrow.

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1 Executive Summary

SANKALPANA 2026 was a Mini Project Exhibition and Competition successfully organized by the Department of Electronics and Telecommunication Engineering (EXTC) at Don Bosco Institute of Technology (DBIT), Kurla, Mumbai. Held on 14 May 2026, the event brought together 119 Second Year (SE) and Third Year (TE) students who designed, built, and presented functional mini projects in the domains of embedded systems, IoT, automation, digital electronics, and communication systems.

The initiative was organized under the banner of IETE-DBIT-ISF and coordinated by Mr. Ankur Ganorkar. The event provided students a vibrant platform to bridge the gap between theoretical knowledge and practical engineering, while developing communication, teamwork, and innovation skills.

Parameter	Achievement
Total Participants	119
Girl Participants	37
Boy Participants	82
Year Groups	SE (Second Year) & TE (Third Year)
Department	Electronics & Telecommunication Engineering
Organized By	IETE-DBIT-ISF
Faculty Coordinator	Mr. Ankur Ganorkar
Venue	EXTC LAB 02, 03,04 and 07
Major Themes	Embedded Systems · IoT · Automation · Digital Electronics, FPGA

2 Event Overview

Particular	Details
Event Name	Sankalpana 2026 – Mini Project Exhibition & Competition
Department	Electronics and Telecommunication Engineering
Organised By	IETE-DBIT-ISF
Faculty Coordinator	Mr. Ankur Ganorkar
Date	14th May 2026
Venue	EXTC LAB 02, 03,04 and 07
Inauguration	9:30 AM – EXTC CR-2 (Chief Guest: Dr. Amiya Tripathi)
Exhibition Timing	10:00 AM – 1:00 PM
Valedictory Function	2:00 PM – 4:00 PM (Seminar Hall)
Patron	Fr. Nelson Carvalho
Chief Guest	Dr. Amiya Tripathi
Target Audience	SE & TE Students of EXTC

The exhibition commenced with an inauguration ceremony at EXTC CR-2 at 9:30 AM in the presence of Father Nelson Carvalho and Chief Guest Amiya Tripathi. The head on the department Dr. Madhavi Pednekar welcomed Father, our guests, faculties and projects members. The event was blessed by a prayer from Father Nelson, followed by a motivational speech by our chief guest Dr. Amiya Tripathi. Faculty members and students attended enthusiastically, marking the formal beginning of the exhibition. Following the exhibition (10:00 AM – 1:00 PM), the valedictory function was held in the Seminar Hall from 2:00 PM to 4:00 PM, where winners were announced and awarded certificates.

3 Objectives & Outcomes

Objectives

- To provide a platform for SE and TE students to showcase their innovative mini projects and technical ideas.
- To encourage students to apply theoretical knowledge in practical engineering applications using embedded systems, sensors, and communication modules.
- To promote creativity, teamwork, and problem-solving skills through project-based learning.
- To give students exposure to project presentation, technical explanation, and evaluation by faculty judges.
- To motivate students to explore modern technologies such as ESP32, Arduino, IoT systems, sensors, automation, and digital electronics.

Outcomes

- Participants successfully demonstrated working mini projects based on embedded systems, communication, automation, and digital electronics.
- Students improved their practical understanding of circuit design, sensor interfacing, and real-time applications using ESP32, Arduino, ultrasonic sensors, temperature sensors, LCDs, and communication modules.
- Participants developed confidence in explaining technical concepts and presenting their projects before judges and faculty members.
- Students enhanced their teamwork, innovation, and project development skills through hands-on implementation and competition.

4 Participation Summary

A total of 119 students from the Electronics and Telecommunication Engineering Department participated in Sankalpana 2026, spanning both Second Year (SE) and Third Year (TE) cohorts. The event saw active representation from 37 girl participants and 82 boy participants, reflecting inclusive engagement across the department.

Category	Count	Percentage
Girl Participants	37	31.1%
Boy Participants	82	68.9%
Total Participants	119	100%

SANKALPANA SE-JUDGES 2026 Judges Panel and Evaluation Guidelines

Panel No.	Venue	Group No.	External Judge
Panel 1	EXTC LAB 2&3 (C-Wing, First Floor)	1 to 9	Dr. Royal D'Souza
Panel 2	EXTC LAB 2, 3 &7 (C-Wing, First Floor)	10 to 18	Dr. Revathy Sundararajan

Panel No.	Venue	Group No.	Internal Judge
Panel 1	EXTC LAB 2&3 (C-Wing, First Floor)	1 to 6	Ms. Freda Carvalho
Panel 2	EXTC LAB 2 &7 (C-Wing, First Floor)	7 to 12	Dr. Sonal Patil
Panel 3	EXTC LAB 2, 3 &7 (C-Wing, First Floor)	13 to 18	Dr. Shilpa Gaikwad

Evaluation Link: <https://forms.gle/DbQyUQetzQpyDzKN8>

Evaluation Rubrics: 10 Marks Each, Total Marks: 50

Sr. No.	Evaluation Criteria	Maximum Marks
1	Social Impact & Community Need	10
2	Project success is defined by clear metrics. Findings are actionable and directly implemented to solve a community issue	10
3	Clarity of Communication & Data Presentation (Combined Focus) : Complex ideas and data are translated into simple, accessible language for the community.	10
4	Poster Design & Visual Architecture- Evaluates the logical structure, data visualization, and spatial organization of the poster layout.	10
5	Prototype presentation is complete and working	10
	TOTAL MARKS	50

SANKALPANA TE-JUDGES 2026
FPGA based Projects -TE EXTC -SEM-VI
Judges Panel and Evaluation Guidelines

Panel No.	Venue	Group No.	Judge 1(External)	Judge 2(Internal)
Panel 1	EXTC LAB 4/6/7 (C-Wing, First Floor)	1 to 10	Mr. Yogesh Gholap	Dr. Aparna Telgote
Panel 2	EXTC LAB 4/6/7 (C-Wing, First Floor)	11 to 19	Dr. Prasad Padalkar	Prof. Ankur Ganorkar

Evaluation Link: <https://forms.gle/i95z2XftGBKo3kdd7>

Evaluation Rubrics: 10 Marks Each, Total Marks: 50

Sr. No.	Evaluation Criteria	Rubrics Description	Maximum Marks
1	Problem Identification and Planning	Problem Statement, Objectives, Creativity, and Innovation	10
2	Methodology, Analysis and System Design	Block/circuit diagram/Flow chart, and hardware/software design logic used	10
3	Implementation, Prototype and Working	Output and live demonstration	10
4	SDG Social and Industrial Relevance	project coverage, and alignment with Sustainable Development Goals	10
5	Teamwork, Presentation and Poster	Poster content, presentation skills, and collaborative effort	10
	TOTAL MARKS		50

Year Group	Details
Second Year (SE)	Projects on embedded systems, IoT, automation, and sensor applications
Third Year (TE)	FPGA projects on digital electronics, communication systems, and signal processing

Students utilized a wide range of modern technologies and components including ESP32, Arduino microcontrollers, ultrasonic and temperature sensors, LCD displays, buzzers, motors, communication modules (UART, GSM), and various other electronic devices to implement their project ideas successfully.

5 Project Highlights & Winners

The exhibition featured innovative projects integrating modern technologies such as Embedded Systems, IoT, Automation, Digital Electronics, Communication Systems, and Sensor Technologies. Faculty members evaluated projects based on innovation, technical implementation, functionality, presentation quality, and conceptual understanding.

SE Sankalpana 2026 Winners

Position	Project Title	Team Members
 1st Position	Traffic Signal Management System	Sapnakumari Giri, Neha Haldankar, Musham Hasan, Amey Kanade
 2nd Position	Smart Irrigation	Yash Khanvilkar, Rohit Kumar, Harshit Patil, Tirth Shetty
 3rd Position	Medicine Reminder and Monitoring Device for Elderly	Kenneth Dsouza, Disha Mishra, Raj Vats, Sneha Savekar





TE Sankalpana 2026 Winners

Position	Project Title	Team Members
 1st Position	PWM Generator for Motor Speed Control	Atharva Badhe, Nirmiti Bait, Sumit Botle, Aayush Jangam
 2nd Position	8 Bit Barrel Shifter	Samiksha Gaonkar, Priya Gajula, Harshali Gokhale, Yukita Jaiswal
 3rd Position	UART Communication	Pritika Mediboina, Om Kurlekar, Ishant Patil, Parth Pardhesi







Photograph with the winners

Innovation Themes Observed

- Embedded Systems & Microcontrollers: ESP32, Arduino-based automation, motor control, PWM generation
- IoT & Sensor Applications: Smart irrigation, environmental monitoring, real-time sensor data
- Digital Electronics: Barrel shifters, logic-level signal processing, digital circuit design
- Communication Systems: UART communication, data transmission, protocol implementation
- Social Impact Engineering: Elderly care monitoring, traffic management, smart automation

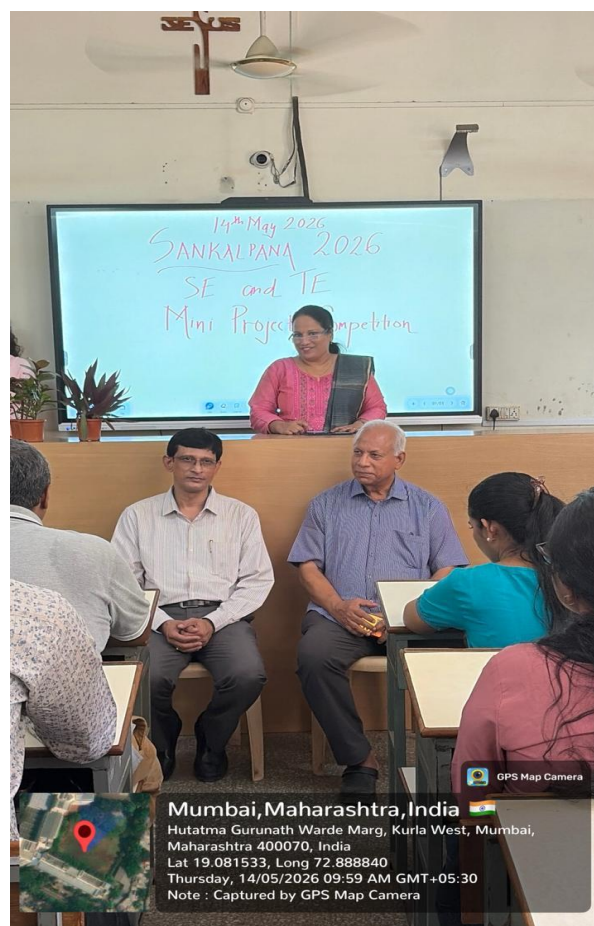
6 Event Proceedings & Photography

Sankalpana 2026 unfolded across a well-structured schedule designed to maximise student engagement and project exposure. The day began with a formal inauguration and ribbon-cutting ceremony, progressed through an active project exhibition, and concluded with a celebratory valedictory function.

Time	Activity	Venue
9:30 AM	Inauguration Ceremony – Welcome by Fr. Nelson Carvalho and Chief Guest Amiya Tripathi	EXTC CR-2
10:00 AM – 1:00 PM	Mini Project Exhibition – Students present projects; interactive evaluation by faculty judges	EXTC LAB 02, 03,04, 07
1:00 PM – 2:00 PM	Lunch Break & Judge Deliberation	—
2:00 PM – 4:00 PM	Valedictory Function – Winner announcement, certificate distribution, closing remarks	Seminar Hall

Inauguration Ceremony

The inauguration ceremony set an energetic tone for the day. Dignitaries, faculty, and students gathered as the Chief Guest performed the formal ribbon-cutting to open the exhibition, followed by a felicitation and motivational address.



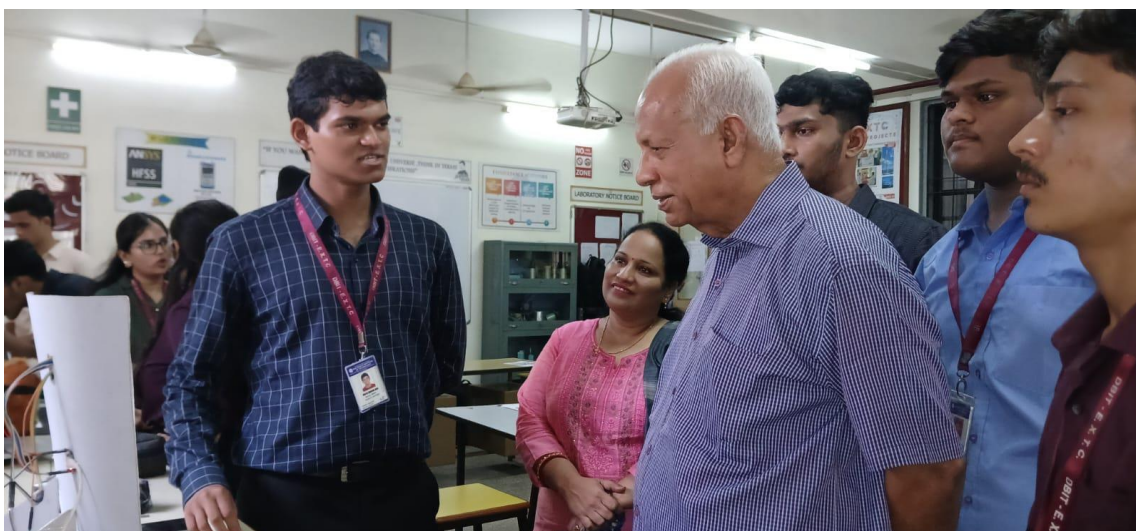


Inaugural session of Sankalpana 2026 – Chief Guest and faculty on the dais, DBIT Kurla, 14 May 2026

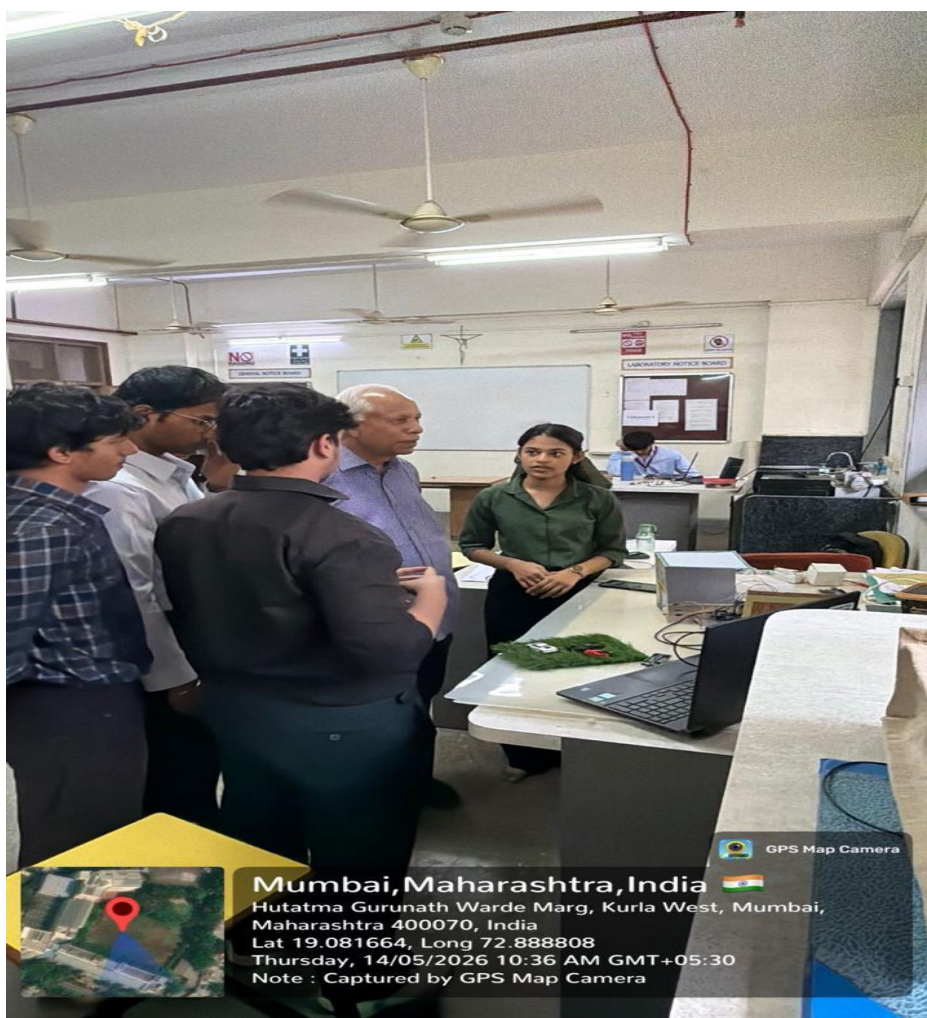
Project Exhibition

During the exhibition phase, students actively explained the objectives, working principles, hardware implementation, and practical applications of their projects to the judges. Faculty members evaluated projects based on innovation, technical implementation, functionality, presentation, and overall conceptual understanding.





Students presenting their projects before faculty judges during Sankalpana 2026 – EXTC Labs, DBIT Kurla



Faculty judge interacting with a student participant during the project evaluation – Sankalpana 2026

7 Key Strengths & Areas for Improvement

Academic Strengths

- High-quality student innovation at SE and TE levels demonstrating strong technical foundations
- Effective integration of practical embedded systems and communication engineering concepts
- Strong interdisciplinary project diversity spanning IoT, automation, and digital electronics
- Active learning pedagogy reinforcing real-world engineering problem solving

Operational Strengths

- Smooth coordination of the inaugural ceremony, exhibition, and valedictory function
- Effective faculty mentoring and structured evaluation ecosystem
- Strong student enthusiasm and participation throughout the event
- Well-managed ribbon-cutting and felicitation ceremonies adding a formal, celebratory atmosphere

Areas for Improvement

- Enhanced display infrastructure for showcasing complex hardware projects
- More pre-event workshops and technical guidance sessions for project preparation
- Expanded participation to include Final Year (BE) students in future editions
- Introduction of industry expert evaluators for broader perspective
- Improved scheduling to reduce inter-session gaps

8 Leadership Remarks

Message from the Patron

"Events like Sankalpana are vital in shaping students who are not just technically proficient but also innovative thinkers who can make a difference in society."

— Fr. Nelson Carvalho, Patron, DBIT

Message from the Chief Guest

"It is inspiring to see students channelling their curiosity into working projects. This is the spirit of engineering — turning ideas into solutions."

— Amiya Tripathi, Chief Guest, Sankalpana 2026

Message from the Head of the department

"Sankalpana 2026 demonstrated that our students possess the drive, creativity, and technical aptitude to tackle real-world challenges. I am proud of every participant who stepped forward to showcase their work."

— Dr. Madhavi Pednekar, HOD-EXTC

9 Organizing Team

Role	Name / Details
Institution	Don Bosco Institute of Technology (DBIT), Kurla, Mumbai
Department	Electronics and Telecommunication Engineering
Organizing Body	IETE-DBIT-ISF (Institution of Electronics and Telecommunication Engineers – Student Forum)
Faculty Coordinator	Mr. Ankur Ganorkar
Patron	Fr. Nelson Carvalho
Chief Guest	Amiya Tripathi

The event was made possible through the collective efforts of the EXTC Department faculty, the IETE-DBIT-ISF student team, and all participating students who invested significant time and effort in developing and presenting their projects.

10 Conclusion & Recommendations

Short-Term Recommendations

- Conduct structured pre-event mentoring workshops to enhance project quality
- Standardize judging rubrics consistently across SE and TE categories
- Improve event logistics and scheduling to maximize productive exhibition time
- Expand documentation and digital archiving of projects for future reference

Medium-Term Recommendations

- Invite industry experts and external evaluators for broader technical feedback
- Introduce an inter-collegiate dimension to Sankalpana in future editions
- Create innovation pathways and incubation support for high-potential projects
- Organize technical training and component familiarization sessions before the exhibition cycle

Long-Term Recommendations

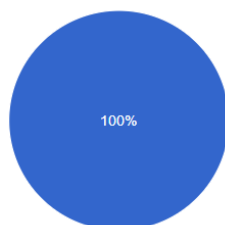
- Develop Sankalpana into the flagship innovation event of DBIT EXTC Department
- Integrate project exhibitions into the department's OBE (Outcome-Based Education) strategy
- Build partnerships with industry and startup ecosystems for project mentoring
- Establish student innovation grants and seed funding for outstanding projects

Impact Area	Outcome
Innovation Platform	Successfully launched departmental mini-project competition for SE & TE
Learning Approach	Encouraged hands-on and experiential engineering education
Student Confidence	Strengthened technical presentation and communication skills
Collaboration	Enhanced teamwork and peer learning among participants
Institutional Quality	Reinforced DBIT EXTC commitment to practical engineering education

EVENT FEEDBACK:

Event Name

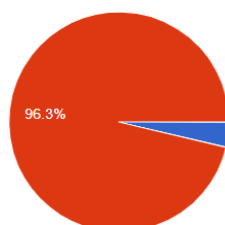
54 responses

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● SANKALPANA 2026

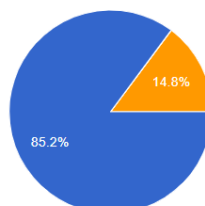
Designation:

54 responses

 [Copy chart](#)● Faculty
● Student
● Others

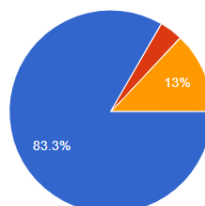
Do You Think The Displayed Projects Were Innovative ?

54 responses

 [Copy chart](#)● Yes
● No
● Maybe

Do You Think The Displayed Projects Satisfied The Social/Environmental/Industrial Needs?

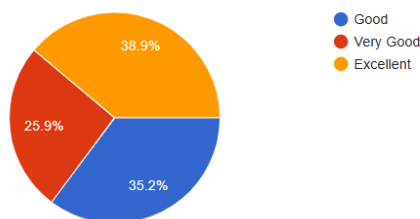
54 responses

 [Copy chart](#)● Yes
● No
● Maybe

How Did You Like The Displayed Projects?

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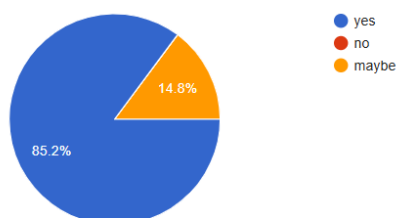
54 responses



Was the event well organized and appropriately conducted?

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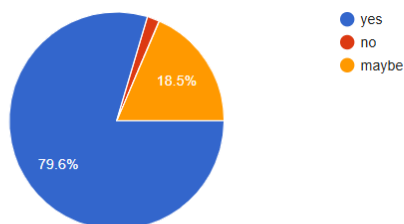
54 responses



Were the students effectively able to showcase their project and presentation skills?

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54 responses



Overall all effectiveness of the event and your comment on the event with suggestions if any?

17 responses

No comments



Nice

5

Na

Good way to integrate tech & practical implementation in students.

Overall good Platform to showcase technical knowledge

Great experience. Projects were great and it was so fun to see all those working models.

It was great opportunity for students to showcase skills

Conclusion

Sankalpana 2026 emerged as a landmark academic event that successfully established a strong platform for hands-on engineering education within the EXTC Department at DBIT. The exhibition demonstrated that SE and TE students, when provided with the right mentorship, environment, and opportunities, are capable of developing meaningful and technically robust solutions. The event reinforced the department's commitment to bridging theoretical learning with practical application, fostering a culture of innovation, collaboration, and engineering excellence.

Report Prepared By: Nandana Anilkumar (Documentation Head)

Report Approved By: Mr. Ankur Ganorkar, IETE-DBIT-ISF Faculty Coordinator

Don Bosco Institute of Technology (DBIT), Kurla, Mumbai · 14 May 2026 · www.dbit.in · NAAC A Grade · Approved by AICTE